

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**



Applicants: Om P. Agrawal; Hans W. Klein; Geoffrey R. Rickard; Harald J. Weller  
Assignee: Lattice Semiconductor Corporation  
Title: Clock Generator With Skew Control  
Serial No.: 10/629,221 Filing Date: 07/29/2003  
Examiner: Unassigned Group Art Unit: 2816  
Docket No.: M-15206 US

Irvine, California  
December 16, 2003

Attn: Official Draftsperson  
COMMISSIONER FOR PATENTS  
Alexandria, VA 22313-1450

**SUBMISSION OF FORMAL DRAWINGS**

Dear Sir:

Applicants submit thirteen (13) sheets of formal drawings, consisting of Figures 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16, 17, 18, and 19, in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (949) 752-7040.

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450, on December 16, 2003.

Eric Hoover

December 16, 2003

Respectfully submitted,

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